

ASSESSMENT METHOD	ASSESSMENT CRITERIA IEE1830
Practical design work at the student lab or at home	<p><i>Assessment criteria</i></p> <p><i>For practical design work at LAB each master level student is given an individual exercise to design a CMOS logic gate and using this particular gate to design a lay-out for a simple logic circuit and a more complex analog- or mixed circuit (amplifier stage, ADC, or sim.). The environment for realizations is MicroWind2.</i></p> <p><i>The practical design work at LAB will be assessed if:</i></p> <ul style="list-style-type: none"> - <i>all the assignments have been solved</i> - <i>there are no serious mistakes or inaccuracies (there can be some minor mistakes)</i> - <i>the LAB reports are presented correctly and according to the requirements</i> <p><i>The practical design work at LAB will account for 20% of merit points for the final examination grade.</i></p> <p><i>For homework first task each student prepares an extended presentation based on the technology overview given at previous lecture. All presentations should be submitted on the day previous to next lecture. At the beginning of the lecture 1..2 students give presentations based on their prepared and submitted material. The presenters are randomly drawn from the group.</i></p> <p><i>The homework presented on time will be assessed, if:</i></p> <ul style="list-style-type: none"> - <i>all the assignments have been solved</i> - <i>there are no serious mistakes or inaccuracies (there can be some minor mistakes)</i> - <i>the homework is presented correctly and according to the requirements</i> <p><i>For homework second task (optional) a student could prepare literature review paper on a previously announced microelectronics topic to be submitted to peer-reviewed sources (journals, conferences). Assessment criteria – paper acceptance. The homework task will account for 10% of merit points for the final examination grade.</i></p>
Web-based Moodle tests (4 tests); all learning outcomes	<p><i>Assessment criteria for e-tests.</i></p> <p><i>Each student must sign up for the course in Moodle environment. The open-book tests take place twice in the semester. Each test consists of theoretical and practical part. The practical part consists of solving of exercises from the specific field. The tests results in merit points form the 60% of merit points for the final examination grade.</i></p> <p><i>Each student can sign up for the final tests without support materials that take place at the end of the semester. Each test consists of theoretical and practical part. The practical part consists of solving of exercises from the specific field. The tests results in merit points form the 10% of merit points for the final examination grade.</i></p>

<p>Exam, all learning outcomes</p>	<p>Assessment criterion for exam. <i>The final result of the exam (100% = 100 points) summarizes the results of four compulsory tests (4x15%), optional conclusive test (10%), compulsory practical design exercises in LAB (20%) and optional review paper (10%).</i> <i>After the communication of initial grades the students have a right to apply for the oral exam to explain his or her solutions and answers as well as on any additional questions. The final grade after the additional oral exam can be higher as well as lower than the initial grade for the tests and design practice..</i> <i>The grades given for the exam are as follows:</i> <i>„0” – 0-50 points;</i> <i>„1” – 51 - 60 points;</i> <i>„2” – 61 - 70 points;</i> <i>„3” – 71 - 80 points;</i> <i>„4” – 81 - 81 points;</i> <i>„5” – 91 -100 points.</i></p>
<p>PREREQUISITES FOR ASSESSMENT</p>	<p><i>The minimum sum of merit points for practical design work is over 11 and minimum sum of the merit points for tests is over 40.</i></p>

